

10/069053

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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10069053	FILING DATE 07/08/2002	CLASS 428	SUBCLASS 44	GAU 1711 4772	EXAMINER GO RR
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****APPLICANTS:** Fujinawa Tohru; Yusa Masami; Nomura Satoyuki; Ono Hiroshi; Watanabe Itsuo; Arifuku Motohiro; Kanazawa Hohiko;

****CONTINUING DATA VERIFIED:**

THIS APPLICATION IS A 371 OF PCT/JP00/05763 08/25/1999

**** FOREIGN APPLICATIONS VERIFIED:**

JAPAN 11-238409 08/25/1999

JAPAN 2000-92978 03/28/2000

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PG-PUB	DO NOT PUBLISH	<input type="checkbox"/>	RESCIND	<input type="checkbox"/>
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners initials			566.41199X00	

TITLE : Wiring-connecting material and process for producing circuit board with the same

U.S. DEPT. OF COMM./PAT. & TM-PTO-439 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED					CLAIMS ALLOWED	
					Total Claims	Print Claim for O.G.
ISSUE FEE		Assistant Examiner			DRAWING	
Amount Due	Date Paid				Sheets Drawn	Figs Drawn
		Primary Examiner			Print Fig.	
TERMINAL					Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE			WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35 Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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